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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Isamu Kobori, et al.

Art Unit : 2822

Serial No.: 10/713,275

Examiner: Kiesha L. Rose

Filed

: November 17, 2003

Title

: METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF

MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM

TRANSISTOR

MAIL STOP RCE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

No fee is believed to be due in connection with the filing of this paper on the Electronic Filing System (EFS). In the event that any fees are due, please apply any charges or credits to deposit account 06-1050.

Respectfully submitted,

Date:

2/21/06

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Substitute Form PTO-1449 (Modified)		Attorney's Docket No. 07977-024004	Application No. 10/713,275	
(Use several sheets if necessary)		Applicant Isamu Kobori, et al.		
		Filing Date November 17, 2003	Group Art Unit 2822	
(37 CFR §1.98(b))				

U.S. Patent Documents Filing Date							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	If Appropriate
miliai	AA	2003-0036222	02/20/2003	Takemura			09/26/2002
	AB	2003-0116766	06/26/2003	Zhang			12/03/2002
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	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)				
Examiner Initial	Desig. ID	Document		
	AQ			
	AR			
	AS			
	AT			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if no	ot in conformance and not considered. Include copy of this form with
next communication to applicant.	Substitute Disclosure Form (PTO-1449)